EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

PUBLICATION NUMBER

05191802

PUBLICATION DATE

30-07-93

APPLICATION DATE

19-02-91

APPLICATION NUMBER

03045330

APPLICANT: NIPPON STEEL CORP;

INVENTOR: SASAKI YUZURU;

INT.CL.

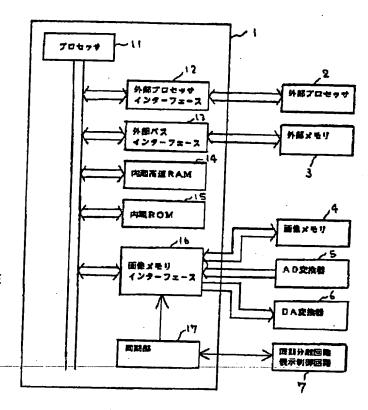
: H04N 7/14 G06F 15/66 H01L 27/10

H04N 1/00 H04N 7/00 H04N 7/08

TITLE

: INTEGRATED CIRCUIT AND PICTURE

TRANSMISSON METHOD USING IT



ABSTRACT :

PURPOSE: To provide an integrated circuit for miniaturizing a still picture transmission device and performing an efficient transmission.

CONSTITUTION: The integrated circuit is composed of a processor 11 for performing the compression and elongating of still picture data an outside processor interface 12 for performing a communication with an external processor 2 connected with the outside, an external bus interface 13 for connecting a memory for storing programs such as a compression and elongation processing, etc., of still picture data, an incorporated ROM 15 storing the basic control programs of the processor, an incorporated high speed RAM 14 for storing programs and data, a picture memory interface 16 controlling a picture memory 4 connecting with the outside and controlling an AD converter 5 and a DA converter 6 and a syncronizing part 17 synchronizing with a synchronizing signal. Thus, by this integrated circuit 1, the miniaturization of a picture transmission device and the efficiency of a transmission is enhanced.

COPYRIGHT: (C)1993,JPO&Japio

BEST AVAILABLE COPY